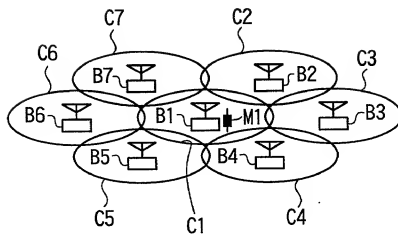
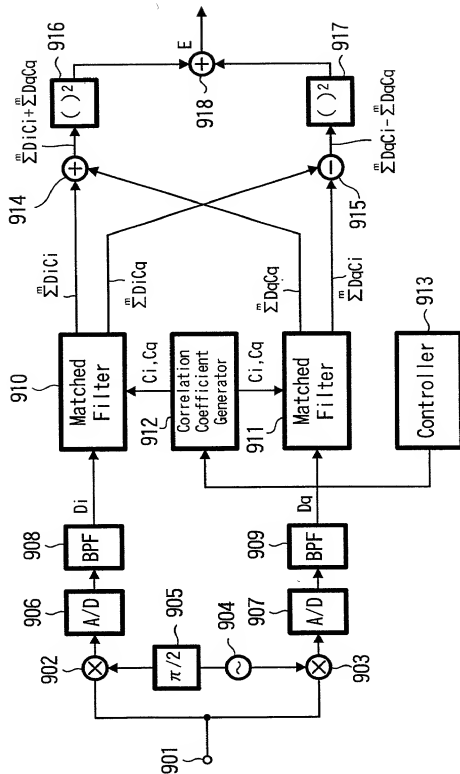


FIG. 1



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FIG. 2



(When The Number of Shift Registers of The Matched Filter is m)

Number of Correlation Added Chips= m
Searchable Range Number= $m \times n$ Chips

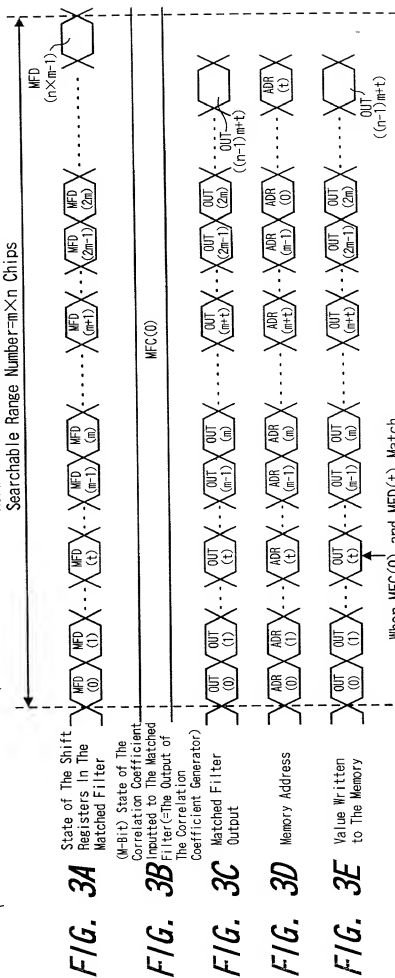


FIG. 4

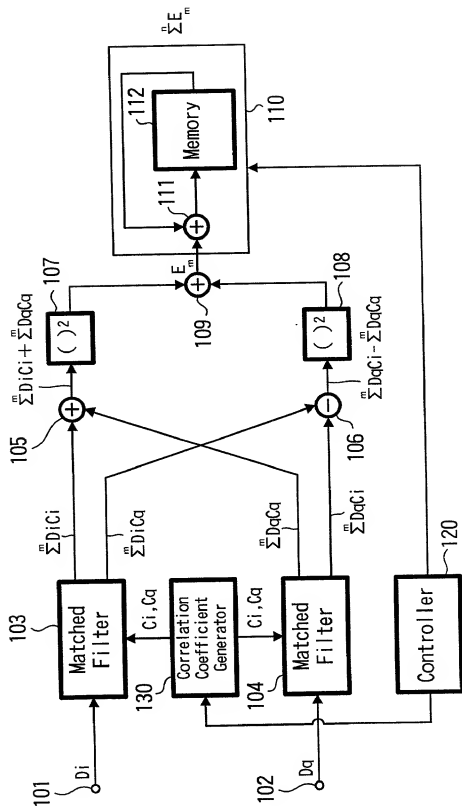


FIG. 5

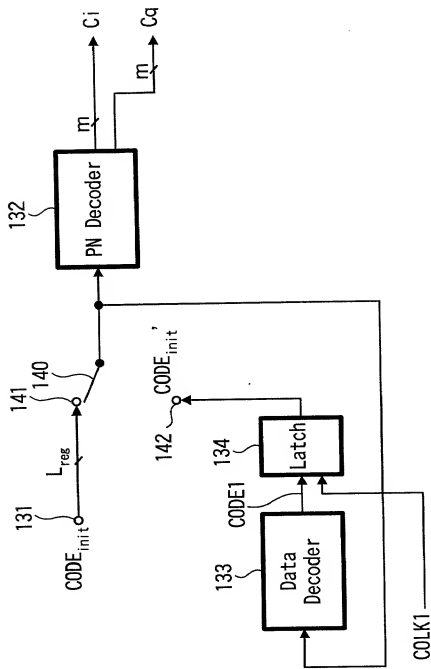


FIG. 6

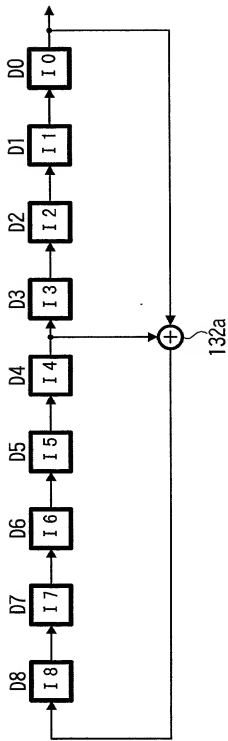


FIG. 7

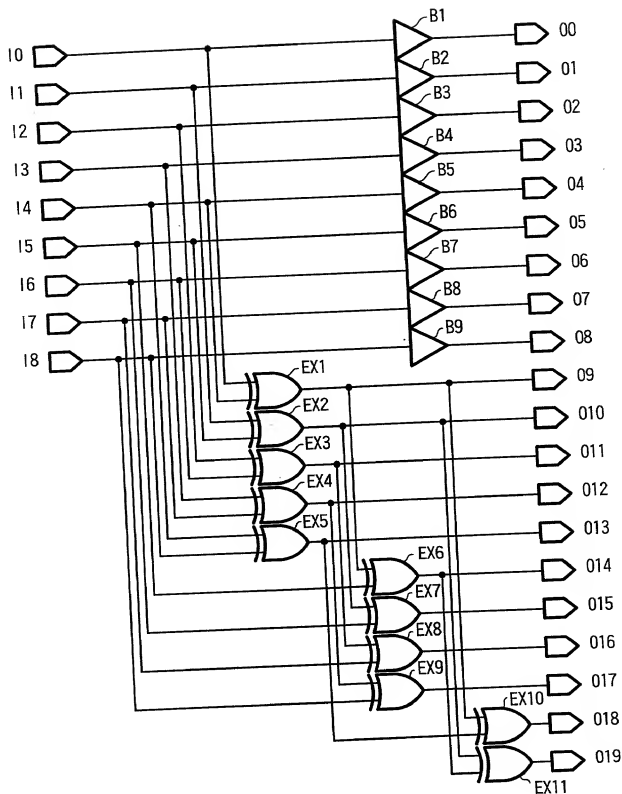


FIG. 8

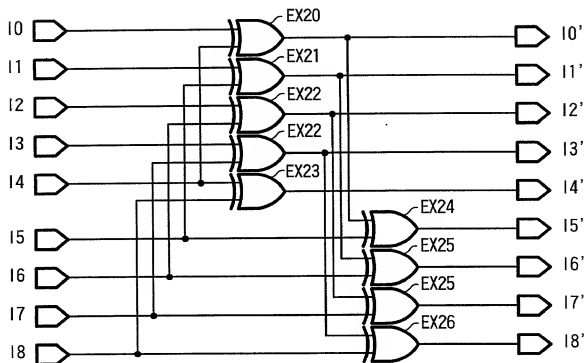
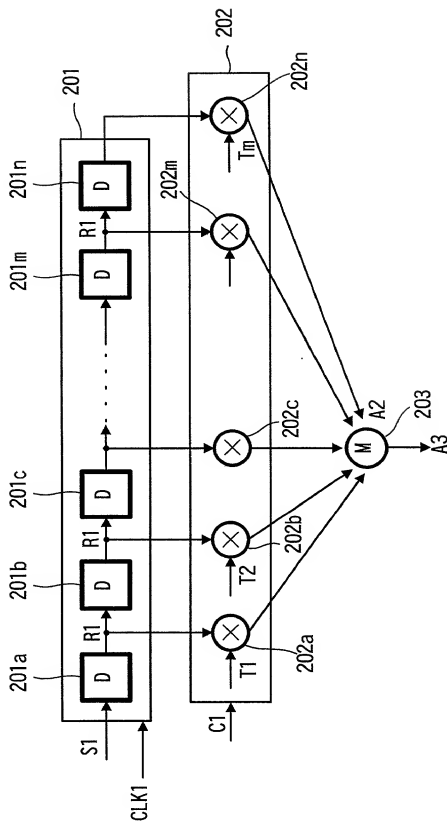


FIG. 9



When The Number of Shift Registers of The Matched Filter is m

Spread Code Cycle $T_{\text{sc}} > m$

Number of Correlation Added Chips= $m \times n$ Chips

Searchable Range
Number=m Chips

FIG. 10A Clock

FIG. 10B

(M-Bit) State of The
Correlation Coefficient
Inputted to The Matched
Filter(=The Output of
The Correlation
Coefficient Generator)

FIG. 10D

FIG. 10E Memory Address

FIG. 10F Value Read From The Memory

FIG. 10G

Local Maximum Value

OUT(m+t)	OUT(2t)
Local Maximum Value	Maximum

Local Maximum Value

FIG. 11

